

What is claimed is:

1 1. A system comprising:
2 a process comprising multiple-mapped memory;
3 a first set of memory mapped onto the multiple-mapped
4 memory,
5 a second set of memory mapped onto the multiple-mapped
6 memory; and
7 an address overload circuit to selectively map the
8 multiple-mapped memory to the first set of memory
9 or to the second set of memory.

1 2. A system as defined in Claim 1, wherein the
2 second set of memory comprises instructions that are
3 effective to execute a protected function.

1 3. A system as defined in Claim 2, further
2 comprising a transfer agent to receive parameters from the
3 process and to assume control of execution of the process
4 when the multiple-mapped memory is mapped to a protected
5 set of memory.

1 4. A system as defined in Claim 3, wherein the
2 transfer agent is effective to call a protected function.

1 5. A system as defined in Claim 4, wherein the
2 transfer agent is effective to call the protected function
3 using parameters received from the process.

1 6. A system as defined in Claim 4, wherein the
2 transfer agent is stored on nonvolatile memory.

1 7. A system as defined in Claim 6, wherein the
2 transfer agent executes on internal memory.

1 8. A system as defined in Claim 3, wherein the
2 address overload circuit comprises:
3 (a) an address multiplexer;
4 (b) an address translator coupled to the address
5 multiplexer; and
6 (c) a data multiplexer.

1 9. A method comprising:
2 executing a process that comprises multiple-mapped
3 memory;
4 determining whether the process is a trusted process;
5 if the process is determined not to be a trusted
6 process, mapping the multiple-mapped memory to
7 unprotected memory; and
8 if the process is determined to be a trusted process,
9 mapping the multiple-mapped memory to protected
10 memory.

1 10. A method as defined in Claim 9, further
2 comprising:
3 storing a transfer agent.

1 11. A method as defined in Claim 10, wherein the
2 transfer agent is stored in a first memory.

1 12. A method as defined in Claim 10, further
2 comprising:
3 determining that the process is a trusted process;
4 copying the transfer agent to a second memory;
5 transferring parameters from the process to the
6 transfer agent; and

7 controlling execution of the process with the transfer
8 agent.

1 13. A method as defined in Claim 12, further
2 comprising:
3 executing the transfer agent so as to identify a
4 protected function and to call the protected function.

1 14. A method as defined in Claim 13, further
2 comprising:
3 executing the protected function.

1 15. A method as defined in Claim 12, further
2 comprising:
3 operating a trust co-processor to determine whether
4 the process is a trusted process.

1 16. A method as defined in Claim 15, further
2 comprising:
3 executing the transfer agent so as to identify a
4 protected function and to call the protected function.

1 17. A method as defined in Claim 16, further
2 comprising:
3 executing the protected function.

1 18. An article comprising a machine-readable storage
2 medium on which there are stored instructions that, if
3 executed, enable a system to:
4 determine whether a process is a trusted process; and
5 if the process is a trusted process, transfer, at
6 least temporarily, control of the process to a
7 transfer agent.

1 19. An article as defined in Claim 18, wherein
2 instructions, if executed, enable the system to transfer
3 process parameters to the transfer agent.

1 20. An article as defined in Claim 19, wherein the
2 instructions, if executed, enable the system to identify
3 and execute a protected function.

1 21. An article as defined in Claim 20, wherein the
2 instructions, if executed, enable the system to copy the
3 transfer agent from nonvolatile memory to volatile memory
4 in the course of executing multiple-mapped memory.

1 22. An article as defined in Claim 18, wherein the
2 instructions, if executed, enable the system to determine
3 whether a process is a trusted process in response to the
4 detection of multiple-mapped memory.

1 23. An article as defined in Claim 22, wherein the
2 instructions, if executed, enable the system to:
3 determine that a process is a trusted process;
4 transfer, at least temporarily, control of the process
5 to the transfer agent; and
6 transfer process parameters to the transfer agent.

1 24. An article as defined in Claim 23, wherein the
2 instructions, if executed, enable the system to:
3 by operation of the transfer agent, identify , call,
4 and execute a protected process.

1 25. An article as defined in Claim 24, wherein the
2 instructions, if executed, enable the system to copy the
3 transfer agent from nonvolatile memory to volatile memory
4 in the course of executing a trusted process that comprises
5 multiple-mapped memory.

1 26. A system comprising:
2 an integrated circuit device comprising a processor,
3 internal random access memory (RAM), and internal read only
4 memory (ROM);
5 unprotected memory;
6 protected memory;
7 a process to execute on the internal RAM, the process
8 comprising multiple-mapped memory, the multiple-mapped
9 memory to be selectively mapped to either the protected
10 memory or the unprotected memory;
11 a trust co-processor to determine whether the
12 multiple-mapped memory is to be mapped to the unprotected
13 memory or is to be mapped to the protected memory;
14 a wireless interface coupled to the processor; and
15 an antenna coupled to the wireless interface.

1 27. A system as defined in Claim 26, further
2 comprising a circuit coupled to the trust co-processor to
3 selectively map the multiple-mapped memory to the protected
4 memory.

1 28. A system as defined in Claim 27, wherein the
2 circuit comprises:
3 (a) an address multiplexer;

4 (b) an address translator coupled to the address
5 multiplexer; and
6 (c) a data multiplexer.

1 29. A system as defined in Claim 26, further
2 comprising a transfer agent to receive parameters from a
3 trusted process, call a protected function using the
4 parameters, and cause the protected function to execute.

1 30. A system as defined in Claim 29, further
2 comprising a circuit coupled to the trust co-processor to
3 selectively map the multiple-mapped memory to the protected
4 memory.